

IN THE ABSTRACT

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An over-current protection circuit of a semiconductor circuit including a power transistor for outputting current output, a first output current detection circuit and a current limiter circuit for limiting the output current by stopping the output current of the power transistor for a predetermined period in response to a first detection signal from the output current detection circuit when the output current of the power transistor reaches a predetermined limit value. The over-current protection circuit has an output current detection transistor current-mirror connected to the power transistor and a second output current detection circuit for generating a second detection signal according to an output current of the output current detection transistor when the output current reaches the predetermined limit value. Further, the output current of the power transistor is cut off for the predetermined period by operating the current limiter circuit in response to the second detection signal.